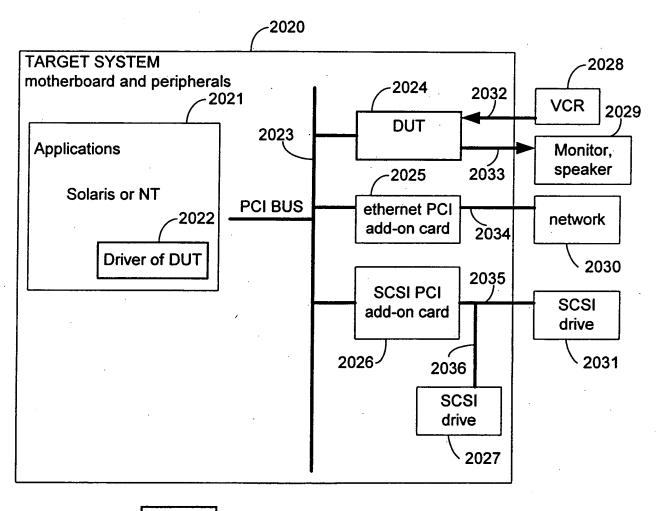
REPLACEMENT SHEET

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Typical Hardware/Software Co-Verification



: DUT (Device Under Test)

FIG. 65

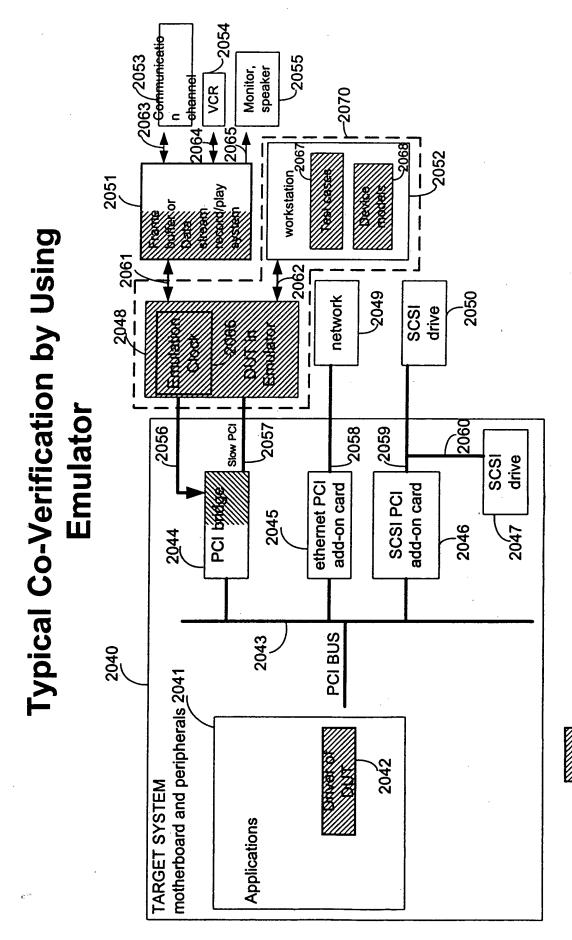


FIG. 66

: running time at emulation speed

The rest of the target system is running at full speed.

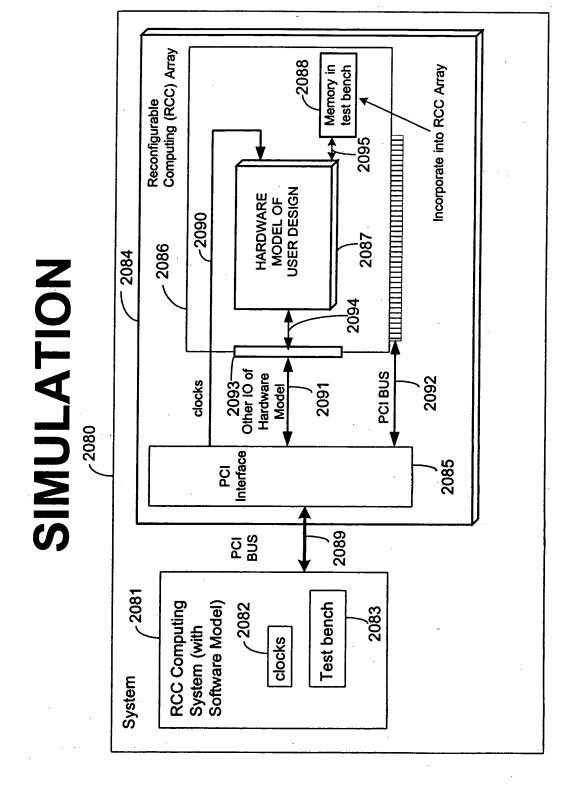


FIG. 67

CO-VERIFICATION WITHOUT EXTERNAL I/O Memory in test bench Reconfigurable Computing (RCC) Array clocks HARDWARE MODEL OF USER DESIGN 72112 -2108 PCI BUS of DUT 21/18 -2100 2116 ~2115 -2109 Interface ဂ္ဂ 2114 PC BUS -2105 RCC Computing System (with Software Model) ~2103 -2104 2106 Driver 2 Device model2 -2101 Device model1 target design Driver of Applications **Test Cases** Driver 1 clocks Target System

FIG. 68

CO-VERIFICATION WITH EXTERNAL I/O

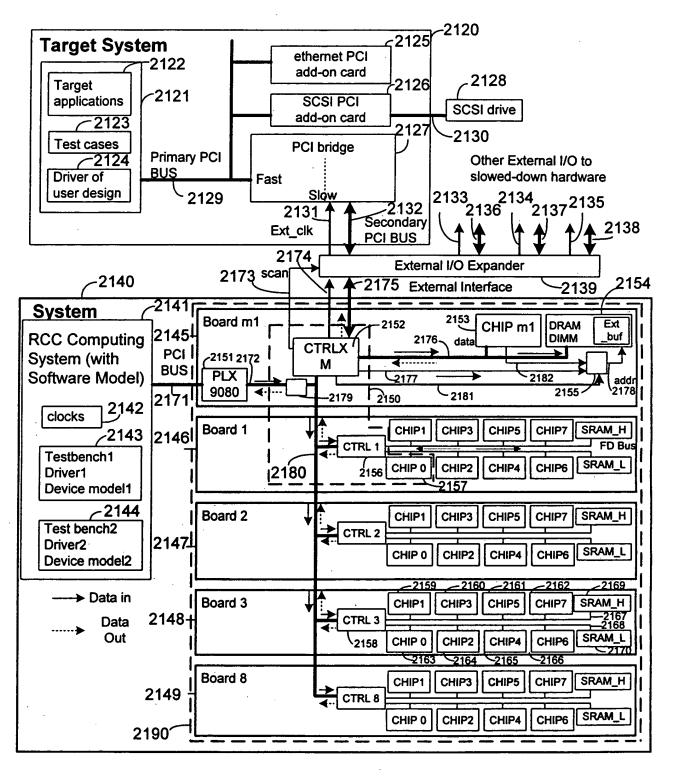


FIG. 69